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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: CHAKIHARA, et al.

Serial No.: 10/629,733

Filed: July 30, 2003

For: A SEMICONDUCTOR EMORY DEVICE AND A METHOD OF

MANUFACTURING THE SAME, A METHOD OF

MANUFACTURING A VERTICAL MISFET AND A VERTICAL

MISFET, AND A METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE AND A SEMICONDUCTOR

DEVICE

<u>INFORMATION DISCLOSURE STATEMENT</u> <u>UNDER 37 CFR 1.97 & 1.98</u>

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 October 29, 2003

Sir:

Applicants are submitting herewith for the Examiner's consideration copies of documents. The documents are listed on the attached form equivalent to Form PTO-1449.

This Information Disclosure Statement is being submitted within three months of the filing date.

Each of the documents listed on the attached form equivalent to Form PTO-1449 is in the English language.

Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Antonelli, Terry, Stout & Kraus, LLP, Deposit Account No. 01-2135 (Case: 501.42869X00), and please credit any excess fees to such deposit account.

Respectfully submitted,

Gregory E. Montone C Registration No. 28,141

ANTONELLI, TERRY, STOUT & KRAUS LLP

GEM/pay (703) 312-6600

FORM PTO-1449 U.S. Department of Commerce (Rev. 4/92) Patent and Trademark Office										ATTY. DOCKET NO. SER 501.42869X00 10/6					
INFORMATION DISCLOSURE STATEMENT BY APPLICANT										APPLICANT CHAKIHARA t al.					
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